

## **New PAM4 ED Makes Anritsu Signal Quality Analyzer-R MP1900A First BERT to Support Real-time Measurement of FEC Symbol Errors**

*— Enhanced Module Allows MP1900A to Meet Market Need for More Efficient Evaluation of  
Next-Gen 400 GbE /800 GbE Devices and Transceivers —*

**Allen, TX – March 11, 2021** – Anritsu Company introduces expanded Forward Error Correction (FEC) analysis capability in its 116-Gbit/s PAM4 Error Detector (ED) MU196040B module for the [Signal Quality Analyzer-R MP1900A](#) series bit error rate testers (BERTS). With new ED module, the MP1900A is the first BERT to support real-time measurement of FEC symbol errors, providing design engineers a single-instrument solution to more efficiently evaluate next-generation 400 GbE and 800 GbE high-speed devices and transceivers.

The new PAM ED features best-in-class input-sensitivity performance, as well as the real-time capability to detect FEC symbol errors based on the 400 GbE FEC standard. In addition to conventional bit error rate (BER) measurement, the MP1900A also supports jitter tolerance measurements for assessing error correction capability using FEC, as required by transmissions using high-speed PAM4 signals.

For the first time, engineers can monitor changes in bit errors and FEC symbol errors with changes in input amplitude and jitter conditions in real-time with the [MP1900A](#). The result is engineers can quickly and reproducibly evaluate when symbol error counts exceed the correction ability of FEC. Since the new FEC analysis function is compatible with conventional jitter tolerance automatic measurement software, the MP1900A BERT supports one-button jitter tolerance measurements based on whether error correction using FEC is possible.

Anritsu addresses an emerging market need with the new FEC analysis function of the MP1900A. Evaluation of devices and transceivers supporting PAM4 not only requires jitter tolerance and sensitivity evaluations based on conventional bit error and error-free measurements, but also measurement of error correction using FEC. The MP1900A, with the new PAM4 ED module, satisfies these testing parameters.

## **MP1900A BERT Outline**

The [Signal Quality Analyzer-R MP1900A](#) series consists of multichannel bit error rate (BER) measuring instruments. The modular BERTs can inspect and verify next-generation network interfaces, such as 400 GbE and 800 GbE, as well as high-speed bus interfaces, such as PCI Express 4.0/5.0/6.0, USB3.2, USB4™, and Thunderbolt™. In addition to more accurate BER measurements, the MP1900A helps shorten development times for high-speed devices and transceivers.

## **About Anritsu**

[Anritsu Company](#) is the United States subsidiary of Anritsu Corporation, a global provider of innovative communications test and measurement solutions for 120 years. Anritsu's "2020 VISION" philosophy engages customers as true partners to help develop wireless, optical, microwave/RF, and digital solutions for R&D, manufacturing, installation, and maintenance applications, as well as multidimensional service assurance solutions for network monitoring and optimization. Anritsu also provides precision microwave/RF components, optical devices, and high-speed electrical devices for communication products and systems. The company develops advanced solutions for 5G, M2M, IoT, as well as other emerging and legacy wireline and wireless communication markets. With offices throughout the world, Anritsu has approximately 4,000 employees in over 90 countries.

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### **Anritsu Contact:**

Laura Edwards  
Senior Manager, Americas Marketing  
[Laura.edwards@anritsu.com](mailto:Laura.edwards@anritsu.com)

### **Agency Contact:**

Patrick Brightman  
3E Public Relations  
[pbrightman@3epr.com](mailto:pbrightman@3epr.com)  
973.263.5475

