Accelerate OTN Switch Validation with Multichannel Testing

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In today's high-speed core network market, more and more network operators are deploying high-speed optical transport network (OTN) links in response to increased demand for higher bandwidth. OTN technology offers a multitude of benefits, particularly its forward error correction (FEC) mechanisms as well as the efficient traffic grooming and optimal granularity through virtual containers or mapping structures. One of the important types of use of OTN is to transport TDM traffic, for example synchronous digital hierarchy (SDH) or synchronous optical network (SONET). OTN also provides ideal support for packet-based services such as Ethernet and Fibre Channel with its flexible approach of mapping structures via ODU0 and ODUflex as OTN payload. High-speed OTN signals are therefore built upon many smaller bandwidth signal channels as illustrated in Figure 1.



Figure 1: High-speed OTN signals built on smaller bandwidth signals

CHALLENGES

Network equipment manufacturers (NEMs) design multiple types of network equipment that reach higher rates of up to 100G. These devices must correctly handle all the signals that make up the overall link. They face a significant challenge of making sure that the interaction of all composing signals is seamless and that each and every channel is transmitted error-free from end to end (Figure 2). The validation of the entire 100G channel is fundamental at design and validation labs where first class NEMs develop their high speed network elements, ensuring that they deliver high performance equipment that are trustworthy and reliable.



The OTN also offers a protection-switching mechanism that switches traffic from the working path to the protection path in the event of failures. OTN networks typically use a mesh topology and require accurate service disruption time measurements for each and every individual channel that composes the overall high rate signal. As a result, the channels must be simultaneously monitored. Testing tools must be able to use advanced triggers and measure the transition time from the working path to the protection one. The switching time must comply with the ITU G.841 standard, which is 50 ms. In today's metro networks, the mesh (Figure 3) configuration is heavily used where OTN core network switches can cause delays, resulting in channels not switching within the recommended time. It is essential for NEMs to ensure that all channels can be viewed at the same time, and that the service disruption time (SDT) of each channel complies with the specified switching time.



Figure 3: Mesh configuration

Another key element for NEMs is the capability to demonstrate that the bit error rate (BER) of each channel that makes up the high rate pipe complies with the network specifications and therefore meets the expected quality of services. Again, all the channels must be individually monitored and the BER performance evaluated separately. It is paramount to have a testing tool that can perform this evaluation of all the independent channels simultaneously without wasting time. Tools that do not offer the complete high-rate performance evaluation are not worth being used for two reasons. First, all composing channels must be monitored simultaneously to validate any cross channel interference. Second, the time to validate each and every channel individually would be very costly and represent a major burden for NEMs.



Figure 2: End-to-end metro network

Most large carrier networks have aggregation switches deployed at major gateway locations. This architecture is very beneficial and offers the following advantages:

- > OTN switches operating at the OTU layer. These switches are independent of the traffic type carried within the payload. This means that the network becomes transparent to any underlying services and protocols: Ethernet, Fibre Channel, SONET, SDH, etc.
- > Switching at the ODU layer, which allows alarm monitoring and end-to-end performance evaluation.
- Switching scalability, which provides all the necessary resources needed for current traffic and protects the investment of network operators and services providers when additional services and traffic must be added.
- > OTN switching utilizing standard OTN mapping structures. The diversified OTN mapping structures enable efficient grooming of traffic as well as high utilization of the ultra-long-haul wavelengths.
- Guaranteed switching time within standard values and ODU0, ODU1, ODU2, ODU3 and ODUflex granularity.

This application note introduces the Multichannel OTN Test Application, which is now available on EXFO's popular and industry-leading FTBx-88200NGE PowerBlazer and FTB-85100G PacketBlazer. The following sections outline the multichannel generation and monitoring features and capabilities of the application, particularly:

- Validation of individual channel connectivity with a device under test (DUT) connected to the network
- > Basic interoperability
- > Support of ODU0, ODU1, ODU2, ODU3 channels
- > Alarm/Error monitoring
- Single Alarm/Error injection on one single channel or on all channels at a time
- > Concurrent OTN BERT analysis
- > Concurrent SDT measurement
- > Configurable Channels/Tributary slots



Figure 4: FTBx-88200NGE and FTB-85100G

When deploying a new network element on an OTN network, the operation team must make sure that this new element operates according to the standards and fulfills the performance expectations previously specified by the network planning group. The team must also validate that all alarms and errors are correctly and timely reported so that both deployment and troubleshooting tasks are performed at an optimum pace, allowing the fastest possible introduction of a new piece of equipment and a reduced failure time. Hence, NEMs must be able to validate that all these tasks are optimally performed in their laboratories prior to the installation of the network element.

CONNECTIVITY VALIDATION – BASIC INTEROPERABILITY – SUPPORT OF ODUO, ODU1, ODU2 AND ODU3 CHANNELS

Once the physical installation of the new network element is completed, the next task is to validate the logical connectivity of the device. To accomplish this job, the technician must generate a test signal to this device, which is configured as a terminating network equipment (NE) under test (DUT). The technician may use the Multichannel OTN Application to both generate the stimulus and analyze the responses from the DUT.



From a physical configuration point of view, this interoperability test may be set in three different ways:

 Scenario 1: The test equipment (FTBx-88200NGE) directly connected to the Terminating NE (DUT)



Scenario 2: Both the test equipment and the DUT connected to the network



Scenario 3: Multiple DUTs connected to the network with a single test equipment that generates the stimulus. In the following example, the test equipment is connected to the network via an OTU4 link. The interoperability monitoring of multiple DUTs is done via OTU2 links.



The Multichannel OTN Test Application supports single stage multiplexing (Figure 5): ODU0 > ODU4, ODU1 > ODU4, ODU2 > ODU4, ODU3 > ODU4 channels. The technician can specifically select the mapping to be exercised among the following multiplexing options:



Figure 5: FTBx-88200NGE - OTN mapping capabilities

The following table shows the maximum number of channels to fulfill the OTU4 signal.

| Multiplexing | Number of Channels |
|----------------|--------------------|
| OTU4/ODU4/ODU0 | 80 |
| OTU4/ODU4/ODU1 | 40 |
| OTU4/ODU4/ODU2 | 10 |
| OTU4/ODU4/ODU3 | 2 |

One alternative to verify the interoperability between the test equipment and the DUT is by the injection of a stimulus (alarms or errors). Once the test is mounted on the test equipment, for example OTU4/ODU4/ODU0 (Figure 6), the technician starts the test and the test equipment automatically goes to the Results > Summary page (Figure 7). The user must then select in which channel to inject an alarm or an error. The final step is to select the stimulus among multiple options (Figure 8); in the example, ODU0-AIS is selected in channel 56 (Figure 9).



Figure 6: FTBx-88200NGE - Test Configurator



Figure 7: FTBx-88200NGE - Summary page

| ummary SDT Trac | es PT Logger | | [1] | - F18x-882 | JONGE | Power | r Blazer | | | | | | | PASS |
|---------------------------------|-------------------------------------|---------|---------------|----------------|---------|----------|-----------|------|-----|----|----|----|----|----------------|
| est Status | In Prog <mark>ess</mark> | © Pi | uss | Start Time | 5/17 | /2016 08 | :33:28 AI | 4 | | | | | | od 00:00:39 |
| GMP-005 OF | BERT - SD BERT Pattern Loss | T O | PM TT SAPI | NUL XFO ODL | SAPI 01 | | | | | | | | | Stop |
| AIS PUM OCI CSF | Bit Error SDT - Longest Disrupti | on (ms) | Paylo | ad Type (PSI 0 |) | | | | | | | | | Save Report Re |
| ayer hannel <u>56</u> ype | DDUU V | | | | | | | | | | | | | inject Laser |
| larms o | AIS V | | | | | | | | | | | | | |
| ontinuous | AIS | | | | | | | 00 | 004 | | | | | |
| | OCI | | | | 2 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | C Setup |
| | LOK | | | | 2 | 33 | 34 | 35 | 36 | 37 | 38 | 39 | 40 | Results |
| | 001 | | | | 2 | 53 | 54 | - 55 | 56 | 37 | 58 | 39 | 60 | |
| | | | | 61 | 52 | 63 | 64 | 65 | 66 | 67 | 68 | 69 | 70 | % Functions |

Figure 8: FTBx-88200NGE - Alarm/Error options



Figure 9: FTBx-88200NGE - Selected channel and alarm

The Multichannel OTN Application displays the responses coming from the DUT upon the introduction of the stimulus, as displayed in Figures 10 and 11. The technician can easily validate the connectivity between the DUT and the test equipment. This will ensure that the newly introduced network equipment is correctly configured and operating as expected. Another way to validate the connectivity is by using the traces features also available in the Multichannel OTN Test Application.

| Summary Soft Tack PT Logget Tet Status IN Progress IN Regress IN Regres IN Regress IN R | | | | | | | | | [| 1] - F | TBx-882 | OONGE | Powe | r Blaze | é) | | | | | | _ 🗇 🗙 | | |
|--|---|------------|-----------------|--------|------------------|----------------------|------------|-------------|------------|----------|-------------|--------------|-----------|----------|----------|----------|----------|----------|----------|----------|--|--|---------------------------------|
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| Image: starting of UL / DUL COUPARISON CoupArison | AIS OCI LCK TIM BDI GMI BEI BEI | P-CRC | PU CSF | E | SDT 0.0 | AL Erroi - Longes | t Disrupti | on (ms) | P | eyload ' | Type (PSI (|)) ppping | | | | | | | | | Sava Sava Repet Raset Reset Raset Laser | | |
| 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 0 <t< th=""><th></th><th></th><th></th><th>In</th><th>terface /</th><th>OTL / OT</th><th>'U4</th><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th>0</th><th>004</th><th></th><th></th><th></th><th></th><th></th></t<> | | | | In | terface / | OTL / OT | 'U4 | | | | | | | | 0 | 004 | | | | | | | |
| 21 22 23 23 24 45 26 27 28 29 30 31 22 33 24 35 26 27 38 99 40 44 42 43 44 45 46 47 48 49 50 51 22 51 51 55 54 55 54 57 38 99 40 ppcc | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | C Setup | | |
| 41 42 43 44 45 46 47 48 49 50 51 12 53 54 55 57 58 59 60 Attractions Leget | 21 | 22 | 23 | 24 | 25 | 26 | 27 | 28 | 29 | 30 | 31 | 32 | 33 | 34 | 35 | 36 | 37 | 38 | 39 | 40 | Co Results | | |
| Layert COUCH-45(5) (54) Contension 71 72 73 76 77 78 79 80 | 41 | 42 | 43 | 44 | 45 | 46 | 47 | 48 | 49 | 50 | 51 | 52 | 53 | 54 | 55 | 56 | 57 | 58 | 59 | 60 | % Functions | | |
| | In | ject 🌒 | 0 % - | Contin | (Ch 56] uous) | - | ~ | | | | 61 71 | 62 72 | 63 73 | 64 74 | 65 75 | 66 76 | 67 77 | 68 78 | 69 79 | 70 80 | 000 | | |

Figure 10: FTBx-88200NGE - Summary alarm indication



Figure 11: FTBx-88200NGE - Detailed alarms and errors - channel 56

EXFO is recognized for the quality of the graphical user interfaces of its testers. For the Multichannel OTN Test Application, the user interface strategy has been to establish a simple configuration of all the channels and grant access to the results through an optimized Summary page that allows a global view of all channels and layers on one single page.



Figure 12: FTBx-88200NGE - Summary view of all channels: OTU4/ODU4/ ODU3 mapping

| 1 | | | [1] | - FTBx-8820 | DONGE Por | wer Blaze | r | | | - 🗖 🗙 |
|--|--|--|---|--|---|--------------|------|---|----|---|
| Summary S2 Test Status Channel 2 GMP-005 LORIOM AIS OCI LOR MP-005 BD1 GMP-07C BD1 GMP-07C BD1 GMP-07C | Traces P In COL2 C OPU2 AIS CSF | T Loggr Progress BERT Retenuise BERT SDT-Longet Derugtion (r 0.000 | PASS PMSS SAPI DAP PR PR | Start Time TTI Traces ^{No} ₄ , XFO ODU ^{No₄} , XFO ODU ^{NO₄, XFO ODU ^{NO₄, XFO ODU ^{NO₄}}}</sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup></sup> | 5/17/2014 9 5471 02 1 DAPI 02 0 pping | 5 08:48:02 A | M | | | PASS No Alarm Or 00:00:13 Stop Part Last Part Last Part Last Part Last |
| | Int | erface / OTI / OTI M | | | | | 0014 | | | Results |
| | 210 | 010007 0107 0104 | | | | | 0004 | | | K Functions |
| Inject | Bit Error (4 | ch 1] | | | 5 | 7 | 8 | 9 | 10 | 000 |
| P1 TX/RX: C | DTU4 (4 Lanes) | rower 🗻 🔠 | | | | | | | | INT 🕲 |

Figure 13: FTBx-88200NGE - Summary view of all channels: OTU4/ODU4/ ODU2 mapping

| | | | | [1] - | FTBx-882 | OONGE | Power | Blazer | | | | | | | _ 🗖 🗙 |
|---|-------------------|--|-----|---------------------------|--|----------------------------|----------|-----------------------|----------------|----------|-----------------------|----------|----------|----------|---------------------|
| ummary St | OT Traces F | PT Logger | | | | | 2016.00 | F2-70 4 | | | | | | | PASS No Alarm |
| nannel 19 GMP-OOS | OUI @ | BERT - SDT | 0 | PM TTI SAPI | Traces | J SAPI 19 | 2016 08 | .52:30 <i>Pe</i> | | | | | | | 0d 00:00:17 Stop |
| LOFLOM AIS OCI LCK TIM BDI | AIS PLM CSF | Pattern Loss Bit Error SDT - Longest Disruption (0.000 | ms) | DAPI Payloac PRBS t | ^s սլ, XFO ODI I Type (PSI (est signal ma |) DAPI 19 | 6 | | | | | | | | Save Repert Res |
| GMP-CRC BIP-8 BEI | | | | | | | | | | | | | | | inject Laser |
| | Int | erface / OTL / 0TU4 | | | | | | | 00 | 004 | | | | | Setup |
| 1 2 | 3 4 | 5 6 7 | 8 | 9 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | % Functions |
| Inject | Bit Error (| Ch 1] | | | 21 | 22 | 23 33 | 24 34 | 25 35 | 26 36 | 27 | 28 38 | 29 39 | 30 40 | 00 |
| DI THINK | THA (A L sour) | | | | - | and the second division of | L tra | and the second second | and the second | 0.000 | and the second second | - | - | - | (B 19/7 |

Figure 14: FTBx-88200NGE - Summary view of all channels: OTU4/ODU4/ ODU1 mapping

| 6 | | Image: State (State) Image: State) Image: State (State) Image: State) Image: State (State) Image: State) Image: State) | | | | | | | | | | | | | | | | | | |
|------------------------------|---------------|---|---------------|-----------|------------------------------------|------------|----------|------|-------------------------|--------------------------------------|--------------------------|---------|-----------|----|-----|----|----|----|----|---------------------------------|
| Summa Test Sta Channel | ary S stus | DT TI | races I | PT L | ogger ss | | ٥ | PASS | Sta | rt Time | 5/17 | 2016 08 | :53:46 Al | 4 | | | | | | PASS No Alarm Od 00:00:12 |
| GMP | -005 | 000 | OPU0 | | BERT | ERT - SD | т | 0 | API NUL | ices XFO ODI | J SAPI 32 | | | | | | | | | Stop |
| LOFI AIS OCI LCK | LOM | AIS PU CSI | 1 | SDT | Pattern Lo St Error - Longes | t Disrupti | ion (ms) | | Payload Ty PRBS test | , XFO ODI ype (PSI 0 signal ma |) DAPI 3;)) pping | 1 | | | | | | | | Save Load |
| BDI GMP BIP- BEI | -CRC 8 | | | | | | | | | | | | | | | | | | | inject Laser |
| | | | In | terface / | OTL / OT | 104 | | | | | | | | 00 | 004 | | | | | |
| 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | 🔿 Setup |
| 21 | 22 | 23 | 24 | 25 | 26 | 27 | 28 | 29 | 30 | 31 | 32 | 33 | 34 | 35 | 36 | 37 | 38 | 39 | 40 | (2) Results |
| 41 | 42 | 43 | 44 | 45 | 46 | 47 | 48 | 49 | 50 | 51 | 52 | 53 | 54 | 55 | 56 | 57 | 58 | 59 | 60 | % Functions |
| Inj | ect | | Bit Error (1) | [Ch 1] | Ę | 2 | | | | 61 | 62 | 63 | 64 | 65 | 66 | 67 | 68 | 69 | 70 | 000 |
| 55 P1 | TV/PV: | - P | Lanes) | Prover | | | | | | n | 12 | 13 | 74 | 15 | 76 | n | 78 | 79 | 80 | Ø INT |

Figure 15: FTBx-88200NGE - Summary view of all channels: OTU4/ODU4/ ODU0 mapping

The FTBx-88200NGE PowerBlazer and the FTB-85100G PacketBlazer offer a simple and complete user interface that enables the visualization of OTN alarms and errors. The interface also offers a simple and unique monitoring system that detects any alarms and errors on all the channels in real time as indicated in Figure 16:

Channels 1 and 75 have a yellow indication meaning that some alarms or errors have been observed since the test has started.

Channel 53 in red indicates that currently there is an active error or alarm observed on this channel.

The orange color in Channel 33 indicates that this is the currently selected channel and the summary alarms refer to it.

| 6 | | | | | | | | [1 |] - F | TBx-882 | OONGE | Power | Blaze | r. | | | | | | _ 🗖 🗙 |
|---|---|-------------------------|-----------|------------------------------|---|-----------|---------|------------------------------|---|--|---|--------------------|----------|----|-----|----|----|----|----|--|
| Summ Test St Charner GM LOF AIS OCC LOK BDD GM BDP BEI | el 33 c P-005 FLOM I C P-0RC 8 | OU AIS PUA CSF | aces | PT L Pupale SDT 0.0 | BERT BERT Sattern Lo It Erro - Longes D0 | ERT SD | on (ms) | FAIL O SAU DA Pa | S ITTIT PI ^{NI} PI ^{NI} Vload | Zart Time Traces ν _L XFO ODI ν _L XFO ODI Type (PSI (st signal me | 5/17 J SAPI 3: J DAPI 3) pping | /2016 08 3 3 | :54:21 A | | | | | | | FAIL DE Error Od 00:01:56 Stop Core Core Error Core Error Core Error Core Error Core Error Core Error Core Error Core Error Error Core Error |
| 1 | | | Ir | iterface / | OTL/0T | U4 | | | | | | | | 0 | 004 | | | | | |
| 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | © Setup |
| 21 | 22 | 23 | 24 | 25 | 26 | 27 | 28 | 29 | 30 | 31 | 32 | 33 | 34 | 35 | 36 | 37 | 38 | 39 | 40 | () Results |
| 41 | 42 | 43 | 44 | 45 | 46 | 47 | 48 | 49 | 50 | 51 | 52 | 53 | 54 | 55 | 56 | 57 | 58 | 59 | 60 | % Functions |
| | - | | Bit Error | [Ch 53] | | | | | | 61 | 62 | 63 | 64 | 65 | 66 | 67 | 68 | 69 | 70 | |
| In | iject 🔵 | · ··· | (1.0E | -02) | - | ~ | | | | 71 | 72 | 73 | 74 | 75 | 76 | 77 | 78 | 79 | 80 | |
| 1 P1 | TX/RX: | OTU4 (4 | Lanes) | Power | A 🖬 | | | | | - 26 | | | | 12 | | | | 2 | 22 | 🕲 INT 🌂 |

Figure 16: FTBx-88200NGE - Summarized alarm and error indication for all 80 ODU0 channels composing an OTU4 signal

CONCURRENT BERT ANALYSIS

Once the connectivity between the DUT and the test equipment (FTBx-88200NGE) has been verified, the next step is to validate if the overall link meets the performance specifications. The test equipment therefore provides the necessary tools to support concurrent BER testing. The user defines the pattern to be used, for instance PRBS31 (Figure 17), and starts the BER evaluation over a predetermined amount of time. The application tool constantly monitors the traffic and, in case of errors, compare the results with the threshold. Moreover, the performance evaluation is done in all composing channels simultaneously (Figure 18). The Multichannel OTN Test Application provides all the necessary features to accomplish this task, offering also a very easy interface to configure the test.

| <u>8</u> | | [1] - FTBx-88200NGE Power Blazer | | _ - × |
|--|----------------------|--|------------------------------------|-------------------|
| Test Applications | rator Timer System | | | - |
| | | ODU Channels [ODU0] | ୍ | |
| Pattern 📰 No Pattern Analysis (Live) | Pattern | PR8531 v Invert | | |
| Bit Error Pass/Fail Verdict Bit Error R | ate V BER Threshold | 1.0E-12 | | Start |
| Service Disruption | | Pass/Fail Verdict | | Save Report Reset |
| Defect BER ~ | Bit Error | SDT Threshold (ms) | | Load |
| No Defect Time (ms) | 300.0 | | | ک ک |
| | | Modily tributary slots/Channels | | |
| | | | | Setup |
| | | | | 🥮 Kesuts |
| | | | Restore Multi-Channel OTN Defaults | X Functions |
| Global Traces PT | | | | 000 |
| P1 TX/RX: OTU4 (4 Lanes) | Power 🙈 🗃 | | | INT (|

Figure 17: FTBx-88200NGE - BER test configuration

| Summary ODT Taxes PT Logge Tet Status IP Progress PAS Stat Time 5/17/2016 04:51:37 PH Tet Status IP Progress PAS Stat Time 5/17/2016 04:51:37 PH United Status IP Progress IP PAS Stat Time 5/17/2016 04:51:37 PH United Status IP Progress IP PAS Stat Time 5/17/2016 04:51:37 PH United Status IP Progress IP PAS Stat Time 5/17/2016 04:51:37 PH United Status IP PAS Stat Time 5/17/2016 04:51:37 PH 1 Difference IP PAS Stat Time 5/17/2016 04:51:37 PH 1 Difference IP PAS Stat Time 1 < | | | | | | | | [| 1] - FT | Bx-882 | OONGE | Power | Blazer | | | | | | | - | | | | |
|---|---|----------|-----------|---------|-------|-----------|----|---------|-----------|-----------|-----------|----------|-----------|----|-----|----|-----|----|----|--------|----------------------------|------|--|--|
| COLD Bertification DOT DECOME Bertification DOT DECOME DECOME DECOME DECOME DECOME DECOME DECOME DOT DECOME DECOME DECOME DECOME DECOME DECOME DECOME DOT DECOME DECO | mmary 50 st Status annel 58 | T Traces | In Pr | Log | 99er | | 0 | PASS | Sta | art Time | 5/17/ | 2016 04 | :51:37 Pf | 4 | | | | | | | PASS No Alarn 00:04: | 25 | | |
| Care Code Option Bits Code | 0 | 000 | 0 | | BE | ERT - SDI | r | 0 P | M TTI Tra | aces | | | | | | | | | | | Stop | | | |
| LDBUCH Mode < | GMP-005 | OPU | 0 | - | BERT | ~ | | SA | UPI -OL | , XPO ODI | J 544 58 | | | | | | | | | | | | | |
| Decr Cold S07-Longed Daruction (m) Papelad Tage (% 0) 000-0 < | LOFLOM | PLM | | Bt | Error | 15 | 0 | DA | API NUL | XFO ODI | J DAPI SE | | | | | | | | | | | - | | |
| DOUBLING | OCI | CSF | | COT | | Discusti | | Pa | ayload T | ype (PSLO | 0 | | | | | | | | | | | Ľ | | |
| 1014 OCUM Deterfice/ OTI. / 7DM COUM 2 3 4 5 6 7 0 0 2 3 4 5 6 2 0 11 12 2 0 1 1 0 0 <th <="" colspan="2" td="" th<=""><td>LOK</td><td></td><td></td><td>0.000</td><td>)</td><td>Una Upu</td><td>(IIIII)</td><td>1</td><td>RDS USL</td><td>synal ma</td><td>pping .</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>Load</td><td></td><td></td></th> | <td>LOK</td> <td></td> <td></td> <td>0.000</td> <td>)</td> <td>Una Upu</td> <td>(IIIII)</td> <td>1</td> <td>RDS USL</td> <td>synal ma</td> <td>pping .</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td>Load</td> <td></td> <td></td> | | LOK | | | 0.000 |) | Una Upu | (IIIII) | 1 | RDS USL | synal ma | pping . | | | | | | | | | Load | | |
| Z 3 4 5 6 7 9 10 11 12 13 14 15 16 17 18 19 20 27 3 4 5 6 7 9 10 11 12 13 14 15 14 13 10 10 12 27 27 36 25 76 27 28 29 10 11 10 12 13 44 15 16 17 18 39 61 42 43 44 45 46 47 48 49 50 52 53 54 55 59 75 59 64 67 64 67 64 67 64 67 64 67 64 67 64 67 64 67 64 67 64 67 64 67 64 67 64 67 64 <td< td=""><td>TIM</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>8</td><td></td><td></td></td<> | TIM | | | | | | | | | | | | | | | | | | | 8 | | | | |
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| 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 7 RE Enviro (7b.11 | 22 | 27 3 | 24 | 25 | 26 | 27 | 28 | 29 | 30 | 31 | 17 | 22 | 34 | 25 | 36 | 37 | 7.8 | 20 | 40 | A Res | dis. | - | | |
| Bit Error (/h 1) 61 62 63 64 65 66 67 68 69 70 | 42 | 43 4 | 64 | 45 | 46 | 47 | 48 | 49 | 50 | 51 | 52 | 53 | 54 | 55 | 56 | 57 | 58 | 59 | 60 | | | - | | |
| | | | inter ICh | | _ | _ | - | _ | - | 61 | 62 | 63 | 64 | 65 | 66 | 67 | 68 | 69 | 70 | 24 Fun | toohs | ľ | | |
| Inject (1.0E-02.) | Inject 🕥 | C (| 1.0E-02) | 1 | 1 | 2 | | | | 71 | 22 | 72 | 74 | 75 | 76 | 77 | 70 | 70 | 80 | 0 | 0 | | | |
| 14 14 13 14 13 16 11 18 19 80 | | | | - | | | | | | 1 | 14 | 13 | 74 | /5 | 76 | " | 18 | 19 | 30 | | | | | |

Figure 18: FTBx-88200NGE - Simultaneous monitoring of all channels



Figure 19: FTBx-88200NGE - Detailed BER results per channel

SERVICE DISRUPTION TIME MEASUREMENTS

As previously mentioned, one of the key features that NEMs have to verify is the amount of time required for network elements to switch to the protection path. This is validated with the SDT measurement. Once the SDT function is enabled and the selected defect is detected (Figures 20 and 21), the test period begins and the SDT starts measuring the disruption time. If there is an absence of defects for a period exceeding the No Defect Time (which is also user-configurable), the SDT measurement duration will be calculated as the time between the detection of the first defect to the end of the last defect, excluding the No Defect Time. The SDT feature will be automatically ready for another measurement as soon as the next defect is detected.

| 5 | | | [1] - FTBx-88 | 200NGE Power Blazer | | _ 🗖 🗡 |
|--------------------------------|-------------------|-----------------|--------------------|--------------------------------|------------------------------------|------------------|
| Test Applications | Test Configurator | Timer System | | | | |
| | | | ODU Channels (OD | nal | Q | |
| Pattern 🔛 | lysis (Live) | Patter | PRBS31 | ∽ Invert | | |
| Bit Error Pass/Fail Verdict | Bit Error Rate | ✓ BER Threshold | 1.0E-12 | | | start |
| Service Disruption | itoring | | Pass/Fail Verdict | | | E1 🚺 🗠 |
| Defect ODU0 | AIS | | SDT Threshold (ms) | 50.0 | | Save Report Rose |
| No Defect Time (ms) | 300 | .0 | | | | 💽 🙆 |
| | | | M | odify Tributary Slots/Channels | | Inject Laser |
| | | | | | | |
| | | | | | | |
| | | | | | | |
| | | | | | | Setup |
| | | | | | | 🥮 Results |
| | | | | | Restore Multi-Channel OTN Defaults | % Functions |
| Global Traces | РТ | | | | | 00 |

Figure 20: FTBx-88200NGE - SDT configuration

| 8 | | [1] - FTBx-88200NGE Power Blazer | | _ 🗖 × |
|--|-----------------------|----------------------------------|------------------------------------|-------------------|
| Test Applications | rator. Timer System | | | - |
| 2 | | ODU Channels (ODU0) | Q | - |
| Pattern 🔛 No Pattern Analysis (Live) | Pattern | PRBS31 V Invert | | |
| Bit Error Pass/Fail Verdict Bit Error R | tate V BER Threshold | 1.0E-12 | | Start |
| Service Disruption Disruption Monitoring | | Poss/Fail Verdict | | Save Report Reset |
| Defect ODU0 ~ | AIS | SDT Threshold (ms) | | Load |
| No Defect Time (ms) | LOPLOM | | | ک ک |
| | AIS | Modify Tributary Slots/Channels | | Inject Laser |
| | 001 | | | |
| | LOK | | | |
| | BDI | | | |
| | 8IP-8 | | | |
| | BEI | | | Setup |
| | | | | 🤗 Results |
| | | | Restore Multi-Channel OTN Defaults | % Functions |
| Global Traces PT | | | | 000 |

Figure 21: FTBx-88200NGE - Defect selection for SDT measurement

Figure 22 below shows two scenarios of SDT measurements.

a. Supposing the defect type set to AIS with the No Defect Time configured to 300 ms.

The first AIS event lasts 20 ms, followed by an absence of the alarm for 150 ms and another AIS event lasting 45 ms. No other alarm is observed afterwards. Since the No Defect Time is set to 300 ms, the 150 ms interval is not enough for the system to clear the service disruption. Therefore the total SDT is 215 ms (20 + 150 + 45 ms) with one single disruption event being counted.

b. Supposing now a different No Defect Time configuration set to 20 ms. The interval between the two AIS situations is enough to clear the first disruption and trigger a second event when the second occurrence of AIS is detected. In this case, there is a total of two disruptions, the first one with a 20 ms duration and the second once with a 45 ms duration.



Figure 22: Service disruption scenarios based on different No Defect Time configurations If the user selected the Pass/Fail verdict for the SDT measurement and defined the SDT threshold to 50 ms, the first scenario would result in a failing verdict with an SDT measurement of 215 ms. The second scenario would present a passing verdict with two SDT measurements that are below 50 ms.

The simplicity offered by the FTBx-88200NGE graphical user interface allows the user to select the Defect type and define the No Defect Time. The per-channel testing and validation makes it possible to easily detect defective channels for advanced troubleshooting. Because each channel does not need to be re-measured, the overall test time is reduced.

CONFIGURABLE CHANNELS/TRIBUTARY SLOTS

Another important feature available with the Multichannel OTN Test Application is the possibility of decoupling Tx tributary slot/channel assignment from the Rx tributary slot/channel assignment. This offers greater flexibility for network equipment manufacturers to validate different channel to tributary slot assignments.

The graphical user interface (Figure 23) is adjusted to give the optimum approach to configure the test and to allow the user to visualize the results/status.

| | | | | | | | Trit | outary | Slot | s/Chan | nels | | | | × |
|-----------------------------|-------------|------------------|------------|----------|----------|----------|-----------------|--------|------|---------------------|---------------------|---------------------|---------------------|---------------------|---------------------|
| OPU4 Tributa | iry Slot | s | | | | | | | | | | | | | |
| ODU4 Chan | nel Nu | mber | | | 1 | | RX | 1 | гх | | TX = RX | | | | |
| Tributary Po Payload Typ | ort: be: | 1 21 | | | | | 1 1 | 2 | 2 | ³ 3 | 4 4 | 5 5 | 6 6 | 7 7 | 8 8 |
| Legend TS TS: CH CH: | Tribut | ary Sk el Nur | ot mber | | | | 9 9 |) 10 | 10 | ¹¹ 11 | ¹² 12 | ¹³ 13 | ¹⁴ 14 | ¹⁵ 15 | ¹⁶ 16 |
| | | | | | | | 17 1 | 7 | 31 | 19 19 | ²⁰ 20 | 21 21 | ²² 22 | ²³ 23 | 24 24 |
| Channels Av | railable | _ | | ۵ | sian | - | ²⁵ 2 | 5 26 | 26 | ²⁷ 27 | ²⁸ 28 | ²⁹ 29 | ³⁰ 30 | ³¹ 31 | ³² 32 |
| 1 2 | 3 | 4 | 5 | 6 | 7 | 8 | 33 3 | 3 34 | 34 | ³⁵ 35 | ³⁶ 36 | ³⁷ 37 | ³⁸ 38 | ³⁹ 39 | ⁴⁰ 40 |
| 9 10 17 18 | 11 19 | 12 | 13 | 14 | 15 23 | 16 | 41 4 | 1 42 | 42 | ⁴³ 43 | 44 44 | 45 45 | 46 46 | 47 47 | 48 48 |
| 25 26 | 27 | 28 | 29 | 30 | 31 | 32 | 49 4 | 9 50 | 50 | ⁵¹ 51 | ⁵² 52 | ⁵³ 53 | ⁵⁴ 54 | ⁵⁵ 55 | ⁵⁶ 56 |
| 41 42 | 43 | 44 | 45 | 45 | 47 | 48 | 57 5 | 7 58 | 58 | ⁵⁹ 59 | ⁶⁰ 60 | ⁶¹ 61 | ⁶² 62 | 63 63 | ⁶⁴ 64 |
| 49 50 57 58 | 51 59 | 52 60 | 53 61 | 54 62 | 55 63 | 56 64 | ⁶⁵ 6 | 5 66 | 66 | ⁶⁷ 67 | ⁶⁸ 68 | ⁶⁹ 69 | ⁷⁰ 70 | ⁷¹ 71 | ⁷² 72 |
| 65 66 73 74 | 67 75 | 68 76 | 69 77 | 70 78 | 71 79 | 72 80 | 73 7. | 3 74 | 74 | ⁷⁵ 75 | ⁷⁶ 76 | ⁷⁷ 77 | ⁷⁸ 78 | ⁷⁹ 79 | ⁸⁰ 80 |
| 0 | N | lodify | Tribut | ary Sk | ots | ~ | | | | | | OK | : | | Cancel |

Figure 23: FTBx-88200NGE - Tributary Slots/Channels assignment

CONCLUSION

In conclusion, NEMs have access to a new tool that addresses the challenges related to their OTN deployments, namely: testing of all channels that make up their overall link up to 100G with one single test equipment, simultaneous BERT measurement for all channels, concurrent SDT measurements, and channel/tributary assignment. This can be accomplished for all 80 ODU0 channels that make up an OTU4 link. The tool provides multiple features:

- > Detailed per channel information, such as the number of defects, the errored seconds as well as the rate
- Injection of a single alarm or error on one channel or on all channels at the time
- > Concurrent BERT analysis over each and every channel
- > Concurrent SDT measurement on every channel

With this new Multichannel OTN Test Application, NEMs can thoroughly test the network elements that they design, expediting their verification and troubleshooting operations, and ultimately provide first-in-class devices to be deployed in high speed OTN networks.

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